VRW0;< ṂṂ Ŀ QSQ ÆṀ ɥ s ǁ ˀ Silicon NPN transistor in a SOT-89 Plastic Package.

⸗ ề ļ ' ᴥ ı ɔ я ᴥ/Ṃ Ŀ ṇ/ љ 5VD4534D Ӥ ´

High f_T, high V_{CEO}, small flat package, complementary pair with 2SA1201A.

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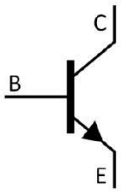
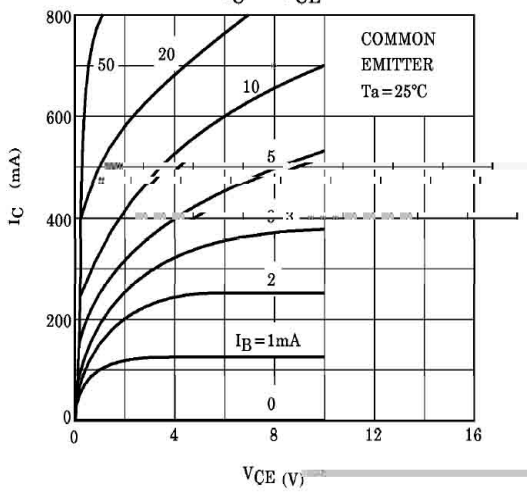


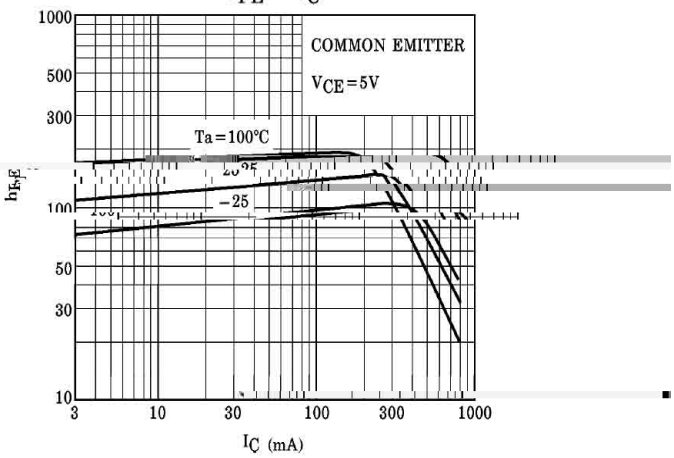
v ˝Ω Parameter	ϒ N Symbol	˝Ω Ö Rating	D Ч Unit
Collector to Base Voltage	V		

/ Electrical Characteristic Curve

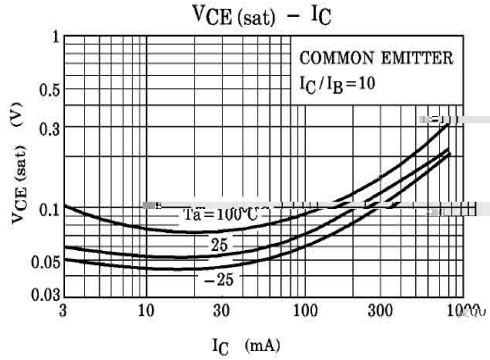
I_C – V_{CE}



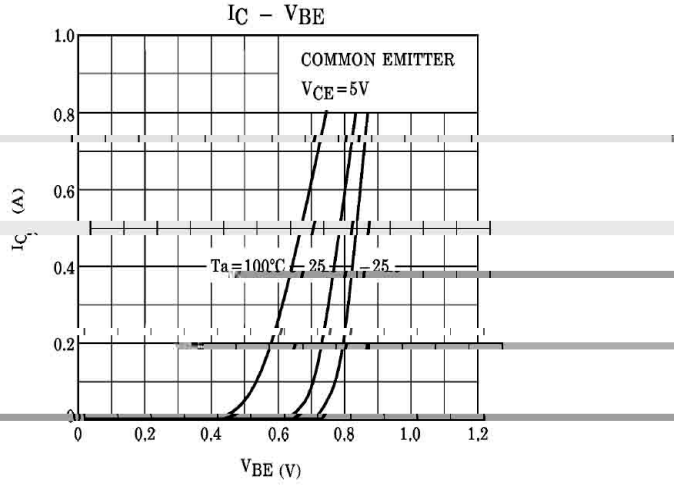
h_{FE} – I_C



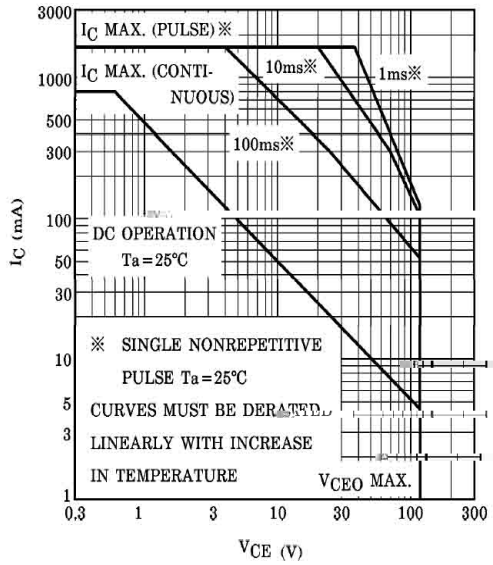
$V_{CE(sat)}$ – I_C



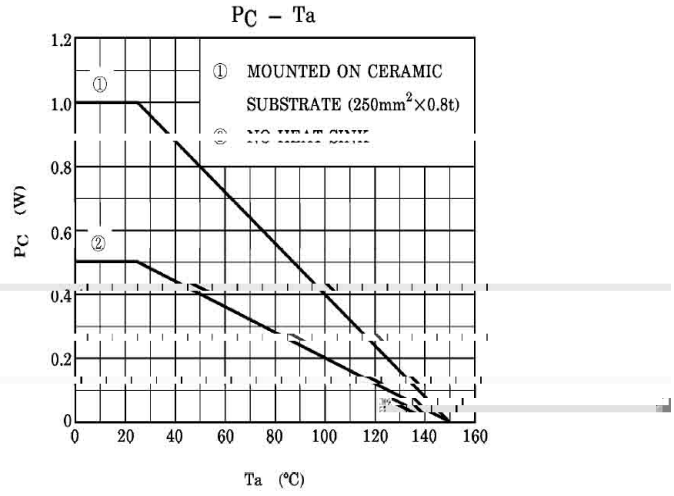
I_C – V_{BE}



SAFE OPERATING AREA

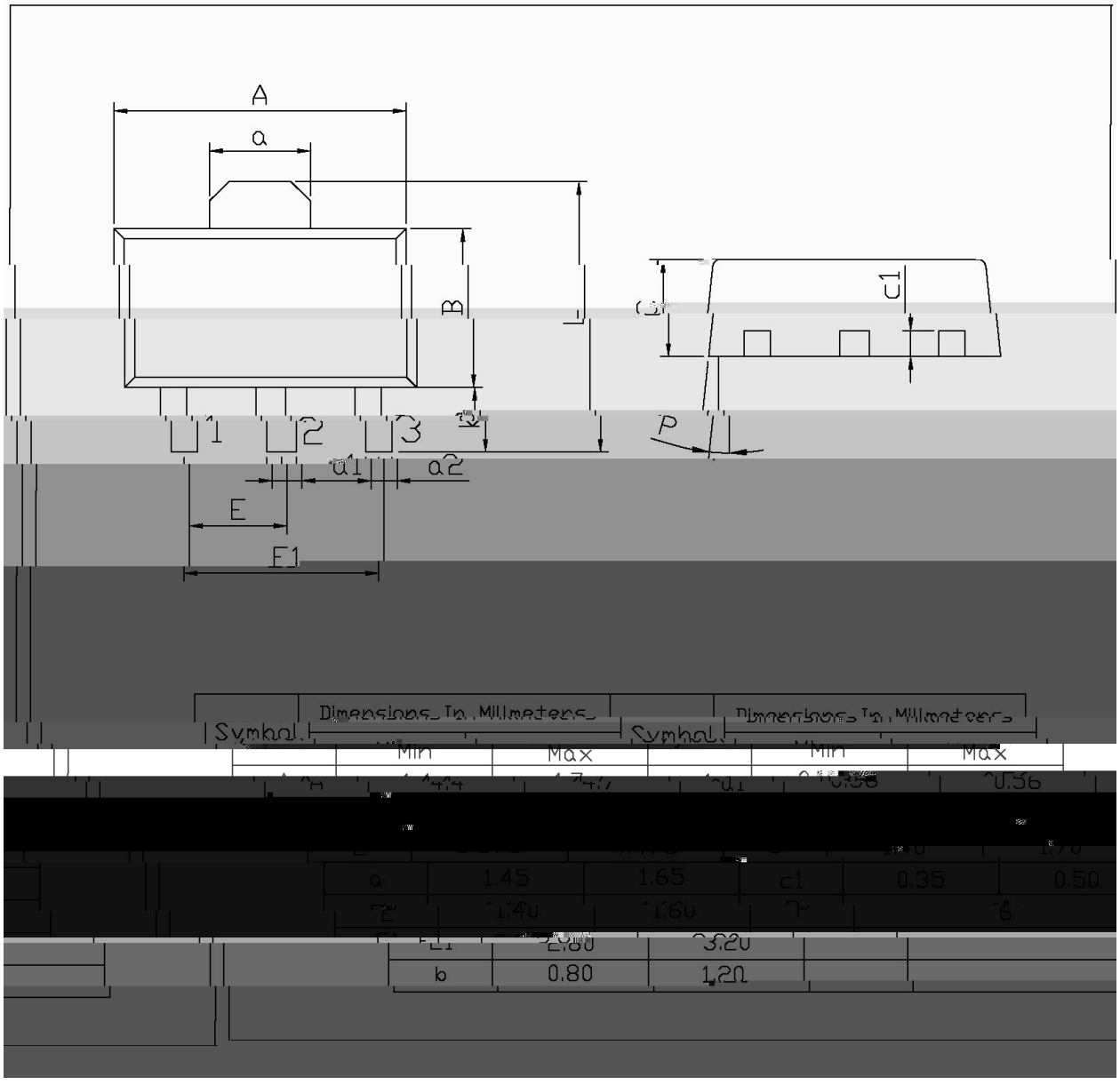


P_C – Ta

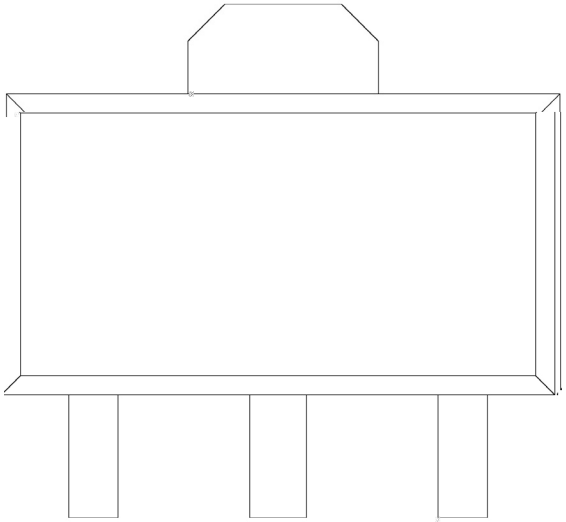


/ Package Dimensions

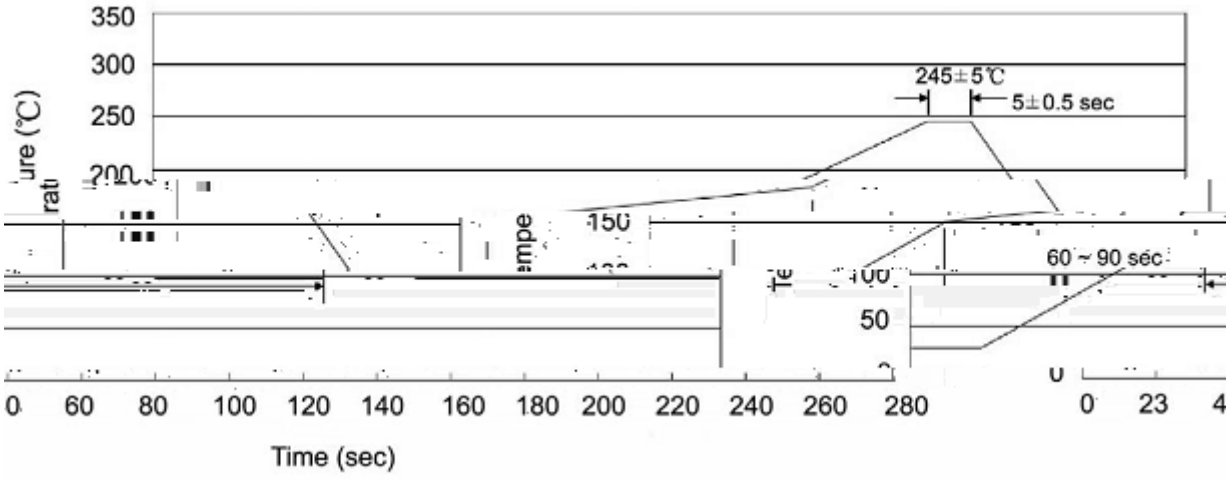
SOT-89 单位：mm



Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters	
	Min	Max		Min	Max
A	1.40	1.60	c	0.35	0.50
a	1.45	1.65	c1	0.35	0.50
a1	1.40	1.60	P	6°	
a2	2.90	3.20			
b	0.80	1.20			



( ) /



ب ˝ χ

1 ℓ ♬ □ ỷ 25 Ḱ 150 ł ı `H ڑ 60 Ḱ 90sec;

2 Š Ö □ ỷ 245 ° 5 ł ı `H ڑ ˋY ⁄ı ⌘ 5 ° 0.5sec;

3 ì ˙ ⅄ ˛ ɔ ⍝ ỷ ⌘ 2 Ḱ 10 ł /sec.

Note:

1.Preheating:25~150 ł , Time:60~90sec.

2.Peak Temp.:245 ° 5 ł , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ł /sec.

/ Resistance to Soldering Heat Test Conditions

□ ỷ χ 260 ° 5 ł `H ڑ χ 10 ° 1 sec. Temp.:260±5 Time:10±1 sec

/ Packaging SPEC.

ɔ` γ ⊢ / REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm^3)		
	只 卷盘	卷盘 盒	只 盒	盒 箱	只 箱		盒	箱

/ Notices